

**United States Patent** [19]  
**Turner**

[11] **Patent Number:** **4,829,227**  
[45] **Date of Patent:** **May 9, 1989**

[54] **HIGH SPEED DATA LINK**

[75] **Inventor:** Jonathan S. Turner, University City, Mo.

[73] **Assignee:** Washington University, St. Louis, Mo.

[21] **Appl. No.:** 139,464

[22] **Filed:** Dec. 30, 1987

**Related U.S. Application Data**

[62] Division of Ser. No. 773,380, Sep. 6, 1985, Pat. No. 4,734,907.

[51] **Int. Cl.<sup>4</sup>** ..... H04Q 11/04

[52] **U.S. Cl.** ..... 370/60; 370/84; 370/94

[58] **Field of Search** ..... 370/60, 84, 85, 94, 370/110.1, 118

[56]

**References Cited**

**U.S. PATENT DOCUMENTS**

4,520,429	5/1985	Grimm et al.	370/84
4,621,359	11/1986	McMillen	370/60
4,641,302	2/1987	Miller	370/60
4,703,475	10/1987	Dretzka	370/60

*Primary Examiner*—Robert L. Griffin

*Assistant Examiner*—Frank M. Scutch, III

*Attorney, Agent, or Firm*—James W. Falk

[57]

**ABSTRACT**

A high bit rate data link is disclosed. The high bit rate data link comprises a group of individual low bit rate data links. A memory stores the addresses of all the low bit rate data links in the group. The memory is indexed by packets including an identification number for the group. The addresses of the low bit rate links are successively written into packets containing the group identification number so that these packets are evenly distributed to the low bit rate links comprising the group.

**4 Claims, 13 Drawing Sheets**

